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**FLE FUSION: Lancer Audio Technology
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MP3 Demonstration Highlights

- FLE FUSION uCLinux 2.4.31 Operating System
- FLE Lancer I2S BusMastering PLB IP-Core Demo (1-hour trial)
- Software MP3 Decode (libMAD 32-bit fixed point)
- FLE NAND Memory Interface IP-Core (FreeFromFLE)
- YAFFS2 NAND FileSystem for 256MB to over 4GB FileSystem Access
- JFFS2 NOR FileSystem for 512KB NOR Flash Access
- FLE Lancer I2S Daughter Card provides Audio & NAND Memory
- FLE LCD IP-Core (FreeFromFLE)
- 10/100 Ethernet (ftp, telnet)
- Pre-Built Reference Design available at www.FL-ENG.com
- Source Reference Design available at www.FL-ENG.com
- Requires Xilinx Spartan3-700AN Eval Board
- Requires FLE Lancer I2S Daughter Card (\$120 at www.FL-ENG.com)
- Supports MP3 Decode upto 48KHz and 64Kb/s rates



FLE FUSION Reference Design Architecture uCLinux, MP3 Software Decode, NAND Flash, I2S Audio

DEMO Details:

- FPGA Design
- Prebuilt U-BOOT
- Prebuilt Kernel
- Prebuilt MP3 Player (libMAD)
- AutoMounts NAND Flash
- Provides 10/100 Ethernet
- FTP and Telnet Access
- 48KHz Sample Rate
- Upto 64Kb/s BitRate
- Files available at FL-ENG.com
- Check the FUSION pages

